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(11)

EP 1 074 949 A1

(12)

EUROPEAN PATENT APPLICATION

(43) Date of publication:
07.02.2001 Bulletin 2001/06

(51) Int Cl. 7: G07C 9/00, G06K 9/00

(21) Application number: 99306179.5

(22) Date of filing: 03.08.1999

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE
Designated Extension States:
AL LT LV MK RO SI

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(54) Integrated circuit board with fingerprint verification capability

(57) An integrated circuit card (1), which is accessed by a card reader (2) that is capable of establishing a communications link with a host computer (3), includes a card body (18), a memory device (11) mounted on the card body (18), a fingerprint sensor (12) mounted on the card body (18), a card reader interface circuit (13) mounted on the card body (18), and a processing unit (14) mounted on the card body (18) and connected to the memory device (11), the fingerprint sensor (12) and the card reader interface circuit (13). The memory device (11) stores fingerprint reference data obtained by scanning a fingerprint of an assigned user, and card in-

formation therein. The fingerprint sensor (12) scans a fingerprint of a holder of the card body (18), and generates fingerprint scan data. The card reader interface circuit (13) is activable so as to communicate with the card reader (2). The processing unit (14) receives the fingerprint scan data from the fingerprint sensor (12), and compares the fingerprint scan data with the fingerprint reference data in the memory device (11) to verify if the holder of the card body (18) is the assigned user. The processing unit (14) activates the card reader interface circuit (13) for exchanging the card information with the host computer (3) via the card reader (2) upon verifying that the holder of the card body (18) is the assigned user.

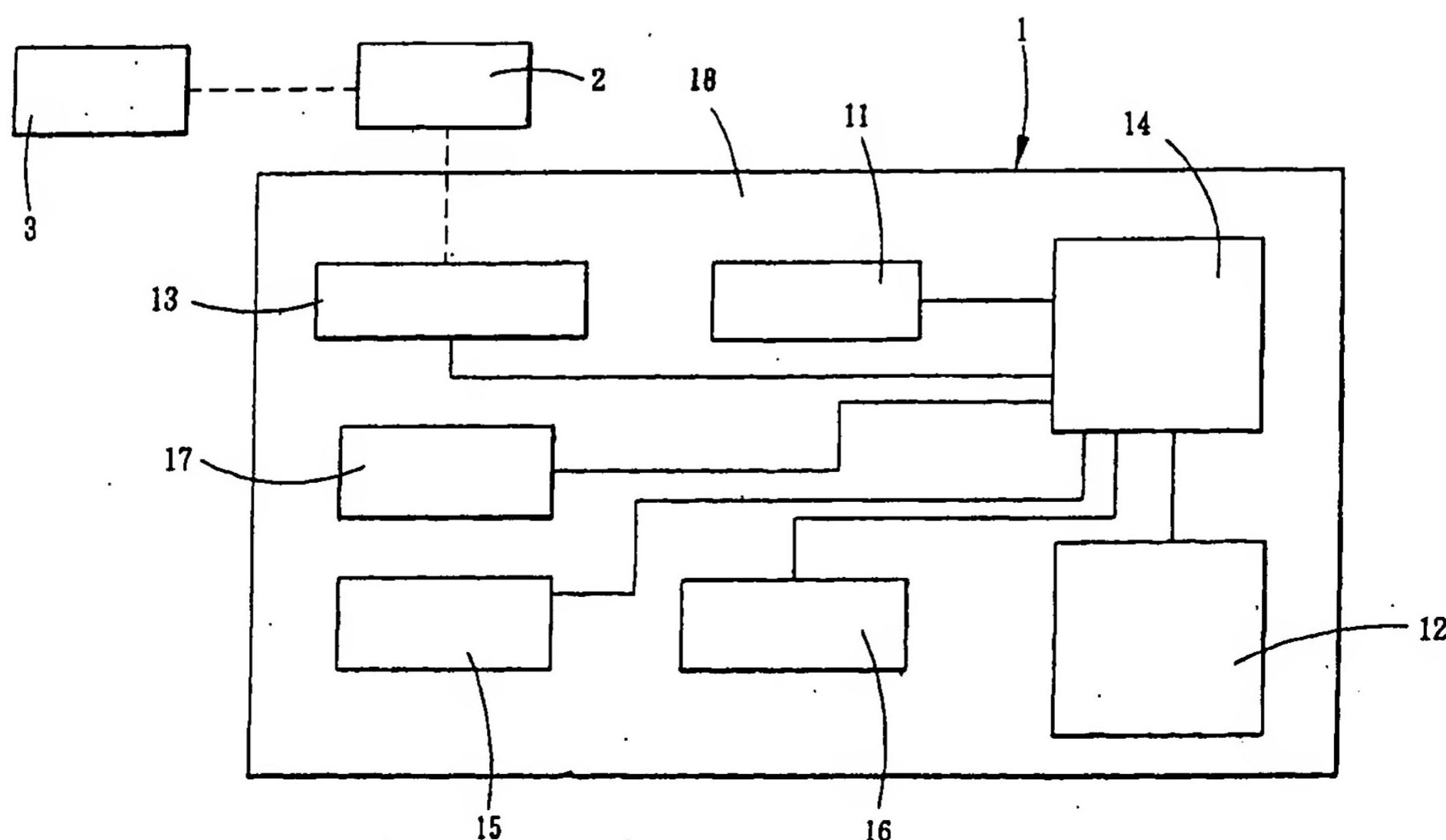


FIG. 1

EP 1 074 949 A1

Description

[0001] The invention relates to an integrated circuit card, more particularly to an integrated circuit card with fingerprint verification capability.

[0002] At present, people have no need to prepare a lot of cash when making purchases, but instead use plastic currencies, such as a credit card, a bank card and so on. Because of the convenience of plastic currencies, it is not uncommon for one to have at least one plastic currency and other cards, such as an identification card and a pass card. When the cards are misplaced or stolen, the owner of the cards must take the risk of misappropriation of the cards.

[0003] Therefore, the object of the present invention is to provide an integrated circuit card with fingerprint verification capability which can reduce the risks involved when the card is stolen or misplaced and which enhances convenience and security when making network transactions.

[0004] According to the present invention, an integrated circuit card is accessed by a card reader that is capable of establishing a communications link with a host computer. The integrated circuit card comprises a card body, a memory device mounted on the card body, a fingerprint sensor mounted on the card body, a card reader interface circuit mounted on the card body, and a processing unit mounted on the card body and connected to the memory device, the fingerprint sensor and the card reader interface circuit. The memory device stores fingerprint reference data obtained by scanning a fingerprint of an assigned user, and card information therein. The fingerprint sensor scans a fingerprint of a holder of the card body, and generates fingerprint scan data. The card reader interface circuit is activable so as to communicate with the card reader. The processing unit receives the fingerprint scan data from the fingerprint sensor, and compares the fingerprint scan data with the fingerprint reference data in the memory device to verify if the holder of the card body is the assigned user. The processing unit activates the card reader interface circuit for exchanging the card information with the host computer via the card reader upon verifying that the holder of the card body is the assigned user.

[0005] Other features and advantages of the present invention will become apparent in the following detailed description of the preferred embodiment with reference to the accompanying drawings, of which:

Fig. 1 is a schematic electrical circuit block diagram illustrating the preferred embodiment of an integrated circuit card according to this invention;

Fig. 2 is a schematic view of a fingerprint scanning area of a fingerprint sensor;

Fig. 3 is a scan line data diagram illustrating a first scanning line (I) in a column direction of Fig. 2;

Fig. 4 is a scan line data diagram illustrating a second scanning line (II) in the column direction of Fig.

2; and

Fig. 5 is a scan line data diagram illustrating a first scanning line (IV) in a row direction of Fig. 2.

- 5 [0006] Referring to Fig. 1, according to the preferred embodiment of this invention, an integrated circuit card 1 is shown to be accessed by a card reader 2 that is capable of establishing a communications link with a host computer 3. The card reader 2 is usually disposed
10 at a public place, such as a shop or a department store, and the host computer 3 is installed at a bank, credit card center and so on. The integrated circuit card 1 includes a card body 18, a memory device 11 mounted on the card body 18, a fingerprint sensor 12 mounted on the card body 18, a card reader interface circuit 13 mounted on the card body 18, a processing unit 14 mounted on the card body 18, a battery 15 mounted on the card body 18, a function key set 16 mounted on the card body 18, and a display unit 17 mounted on the card body 18.
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20 [0007] In the present embodiment, the memory device 11 is a flash memory. The memory device 11 stores fingerprint reference data obtained by scanning a fingerprint of an assigned user, and card information, such as a credit card number, a bank account number and an assigned user identification card number, therein. The fingerprint reference data includes a plurality of scan line data, each of which describes fingerprint characteristics in a respective scanning line of the fingerprint of the assigned user.
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30 [0008] The fingerprint sensor 12 is adapted to scan a fingerprint 5 of a holder of the card body 18 and to generate fingerprint scan data. Referring to Fig. 2, the fingerprint sensor 12 includes an $m \times n$ array of scan cells that defines a fingerprint scanning area (M). The fingerprint scan data includes a plurality of scan line data obtained by scanning corresponding lines of the array of scan cells. The lines of the array of scan cells can be scanned in a column direction or a row direction of the array. For example, if $m=30$, $n=45$, a first scanning line (I) in the column direction is $(1' n; n=1 \sim 45)$, a second scanning line (II) in the column direction is $(2' n; n=1 \sim 45)$, and a thirtieth scanning line (III), the last scanning line in the column direction, is $(30' n; n=1 \sim 45)$. A first scanning line (IV) in the row direction is $(m' 1; m=1 \sim 30)$, a second scanning line (V) in the row direction is $(m' 2; m=1 \sim 30)$, and a forty-fifth scanning line, the last scanning line in the row direction, is $(m' 45; m=1 \sim 30)$. Each of the scan cells generates a high logic signal upon detection of a ridge in the fingerprint 5 of the holder of the card body 18, and a low logic signal upon detection of a valley in the fingerprint 5 of the holder of the card body 18. In Fig. 3, the scan cells $(1' 13)$, $(1' 15)$ generate a high logic signal, respectively, and the other scan cells generate a lower logic signal when the fingerprint sensor 12 scans the first scanning line (I) in the column direction. Fig. 4 illustrates the scan line data obtained by scanning the second scanning line (II) in the column direction.

in the column direction. Fig. 5 illustrates the scan line data obtained by scanning the first scanning line (IV) in the row direction. In view of the unique features of fingerprints, if the card holder is different from the assigned user, the fingerprint scan data will differ from the fingerprint reference data.

[0009] The card reader interface circuit 13 is activable so as to communicate with the card reader 2.

[0010] The processing unit 14 is connected to the memory device 11, the fingerprint sensor 12 and the card reader interface circuit 13. The processing unit 14 receives the fingerprint scan data from the fingerprint sensor 12, and compares the fingerprint scan data with the fingerprint reference data in the memory device 11 to verify if the holder of the card body 18 is the assigned user. The processing unit 14 activates the card reader interface circuit 13 for exchanging the card information with the host computer 3 via the card reader 2 upon verifying that the holder of the card body 18 is the assigned user. This, the integrated circuit card cannot be used if the card holder is not the assigned user.

[0011] The battery 15 is connected to the processing unit 14 and provides electrical power required by the integrated circuit card 1.

[0012] The function key set 16 is connected to the processing unit 14, and is operable so as to select the card information that is exchanged with the host computer 3. For example, when the function key set 16 is selected in a credit card mode, the card information exchanged with the host computer 3 includes the credit card number. Preferably, a segment of the fingerprint reference data stored in the memory device 11 is transmitted by the processing unit 14 to the host monitor 3 upon verifying that the holder of the card body 18 is the assigned user for increased security of network transaction. The segment of the fingerprint reference data includes chosen ones of the scan line data selected according to date or time of the exchange of the card information with the host computer 3. Alternatively, the chosen ones of the scan line data can be selected in a random manner.

[0013] In one example, each scan line data of the fingerprint reference data is numbered, such that the scan line data of an even scanning line in the column direction of the fingerprint scanning area (M) is designated by a number "0", the scan line data of an odd scanning line in the column direction of the fingerprint scanning area (M) is designated by a number "1", the scan line data of an even scanning line in the row direction of the fingerprint scanning area (M) is designated by a number "2", and the scan line data of an odd scanning line in the row direction of the fingerprint scanning area (M) is designated by a number "3". When the selected condition is a time of 2:30, a segment of the fingerprint reference data corresponding to the scan line data of the numbers 2, 3, 0 is accessed by the card reader 2 and is transmitted to the host computer 3 in the 0-2-3-0 sequence. The host computer 3 compares the segment of the finger-

print reference data with the fingerprint reference data stored in the host computer 3 at the selected time condition. In another example, when the selected condition is a time and a date of 4:33 5/3/1999, a segment of the

5 fingerprint reference data corresponding to the scan line data of the numbers 0, 1, 3, 4, 9, 5 is accessed by the card reader 2 and is transmitted to the host computer 3 in the 1-9-9-9-0-5-0-3-0-4-3-3 sequence. Dynamic changing of the segment of the fingerprint reference data ensures that transaction conducted with the use of the integrated circuit card 1 is valid when a particular condition is met, e.g. 4:33 5/3/1999. In other words, when unauthorized copying of the card information transmitted by the integrated circuit card 1 at a particular

10 time and date occurred, use of the card information at another time and date will result in an invalid transaction that will be rejected by the host computer 3.

[0014] The display unit 17 is connected to and is controlled by the processing unit 14 for displaying the card information that is exchanged with the host computer 3 thereon. The display unit 17 can be further used to display information from the host computer 3, such as transacted amount, account balance, etc.

[0015] The integrated circuit card 1 of this invention 20 has the following advantages:

- 25 1. Since the integrated circuit card 1 compares fingerprint scan data obtained by scanning a fingerprint of a holder of the card 1 with the fingerprint reference data in the memory device 11 to verify if the holder is the assigned user, the integrated circuit card 1 can only be used by the assigned user so as to reduce the risks involved when the integrated circuit card 1 is stolen or misplaced.
- 30 2. The card information exchanged with the host computer 3 can vary dynamically since the integrated circuit card 1 further transmits a segment of the fingerprint reference data that is selected according to time and date conditions, thereby further enhancing security when making network transactions.
- 35 3. Since the integrated circuit card 1 can be used as a bank card, an identification card or a credit card, the user only needs to bring along one card to accomplish different tasks, thereby resulting in added convenience.

Claims

- 40 50 1. An integrated circuit card (1) adapted to be accessed by a card reader (2) that is capable of establishing a communications link with a host computer (3), said integrated circuit card (1) characterized by:
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a card body (18);
a memory device (11), mounted on said card body (18), for storing fingerprint reference data

- obtained by scanning a fingerprint of an assigned user, and for storing card information therein;
- a fingerprint sensor (12) mounted on said card body (18) and adapted to scan a fingerprint of a holder of said card body (18) and to generate fingerprint scan data;
- a card reader interface circuit (13) mounted on said card body (18) and activable so as to communicate with the card reader (2); and
- a processing unit (14) mounted on said card body (18) and connected to said memory device (11), said fingerprint sensor (12) and said card reader interface circuit (13), said processing unit (14) receiving the fingerprint scan data from said fingerprint sensor (12) and comparing the fingerprint scan data with the fingerprint reference data in said memory device (11) to verify if the holder of said card body (18) is the assigned user, said processing unit (14) activating said card reader interface circuit (13) for exchanging the card information with the host computer (3) via the card reader (2) upon verifying that the holder of said card body (18) is the assigned user.
2. The integrated circuit card as claimed in Claim 1, further characterized in that said fingerprint sensor (12) includes an $m \times n$ array of scan cells that defines a fingerprint scanning area (M).
 3. The integrated circuit card as claimed in Claim 2, further characterized in that the fingerprint scan data includes a plurality of scan line data obtained by scanning corresponding lines of said array of scan cells.
 4. The integrated circuit card as claimed in Claim 3, further characterized in that the lines of said array of scan cells are scanned in a row direction of said array.
 5. The integrated circuit card as claimed in Claim 3, further characterized in that the lines of said array of scan cells are scanned in a column direction of said array.
 6. The integrated circuit card as claimed in Claim 2, further characterized in that each of said scan cells generates a first logic signal upon detection of a ridge in the fingerprint of the holder of said card body (18), and a second logic signal upon detection of a valley in the fingerprint of the holder of said card body (18).
 7. The integrated circuit card as claimed in Claim 1, further characterized in that the fingerprint reference data includes a plurality of scan line data, each of which describes fingerprint characteristics in a respective scanning line of the fingerprint of the assigned user.
- 5 8. The integrated circuit card as claimed in Claim 1, further characterized by a function key set (16) mounted on said card body (18) and connected to said processing unit (14), said function key set (16) being operable so as to select the card information that is exchanged with the host computer (3).
- 10 9. The integrated circuit card as claimed in Claim 1, further characterized by a display unit (17) mounted on said card body (18) and connected to and controlled by said processing unit (14) for displaying the card information that is exchanged with the host computer (3) thereon.
- 15 10. The integrated circuit card as claimed in Claim 1, further characterized in that said processing unit (14) further transmits a segment of the fingerprint reference data stored in said memory device (11) to the host computer (3) upon verifying that the holder of said card body (18) is the assigned user.
- 20 11. The integrated circuit card as claimed in Claim 7, further characterized in that said processing unit (14) further transmits a segment of the fingerprint reference data stored in said memory device (11) to the host computer (3) upon verifying that the holder of said card body (18) is the assigned user.
- 25 12. The integrated circuit card as claimed in Claim 11, further characterized in that the segment of the fingerprint reference data includes chosen ones of the scan line data selected according to date of the exchange of the card information with the host computer (3).
- 30 13. The integrated circuit card as claimed in Claim 11, further characterized in that the segment of the fingerprint reference data includes chosen ones of the scan line data selected according to time of the exchange of the card information with the host computer (3).
- 35 14. The integrated circuit card as claimed in Claim 11, further characterized in that the segment of the fingerprint reference data includes chosen ones of the scan line data that are randomly selected.
- 40 15. The integrated circuit card as claimed in Claim 1, further characterized in that the card information that is exchanged with the host computer (3) includes a credit card number.
- 45 16. The integrated circuit card as claimed in Claim 1, further characterized in that the card information

that is exchanged with the host computer (3) includes a bank account number.

17. The integrated circuit card as claimed in Claim 1,
further characterized in that the card information 5
that is exchanged with the host computer (3) in-
cludes an assigned user identification card number.

18. The integrated circuit card as claimed in Claim 1,
further characterized in that said memory device 10
(11) is a flash memory.

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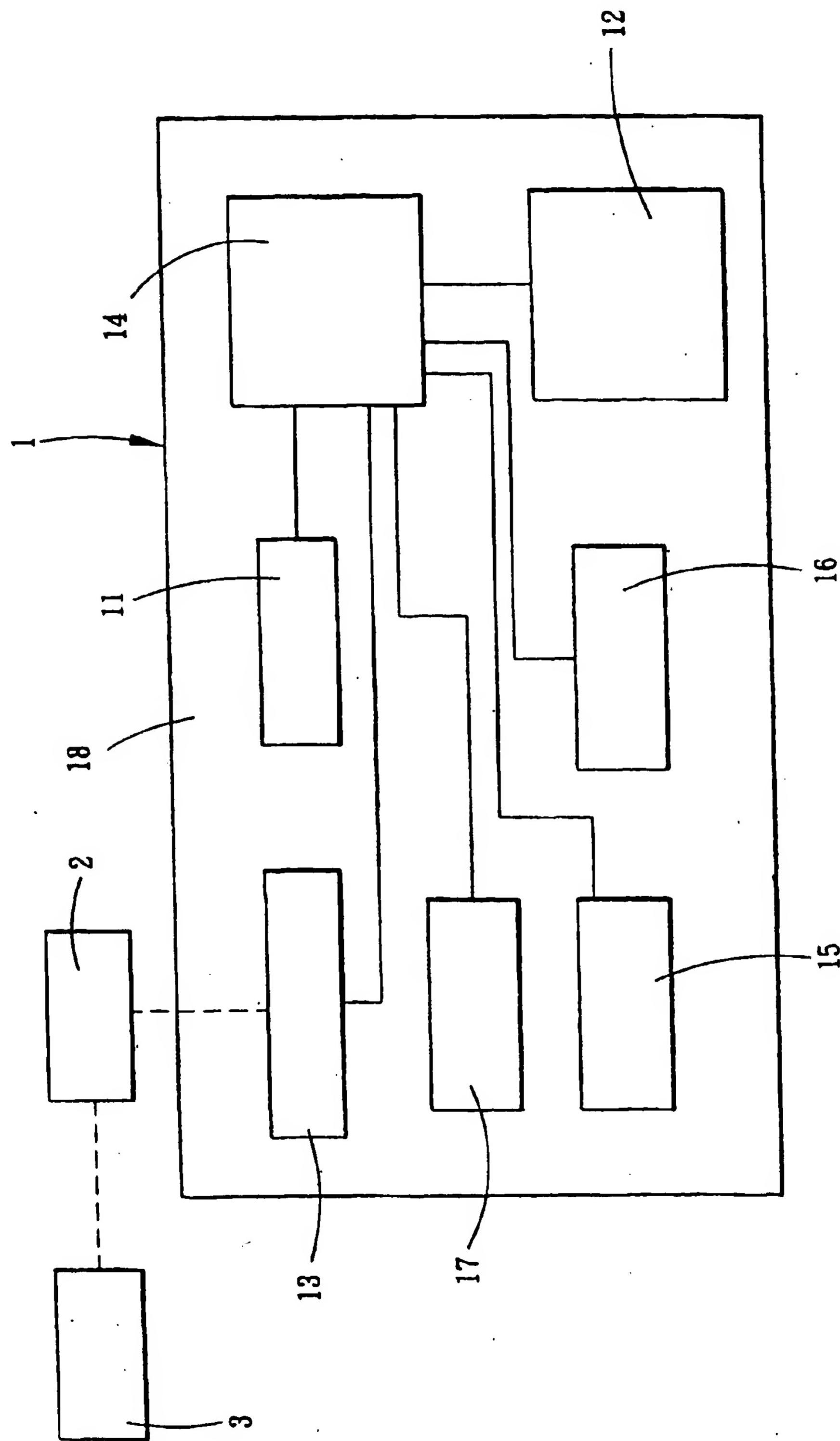
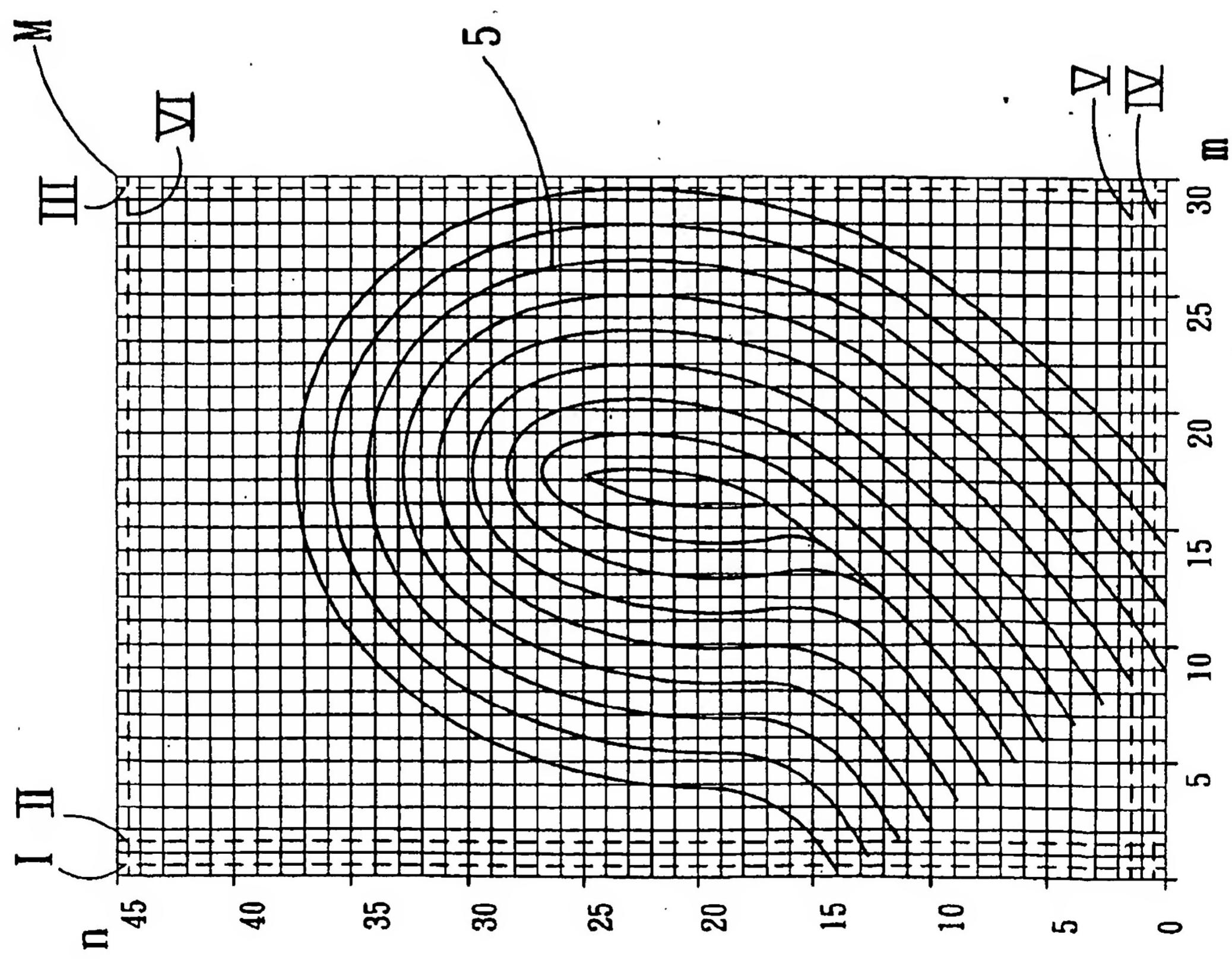


FIG. 1

EP 1 074 949 A1

FIG. 2



EP 1 074 949 A1

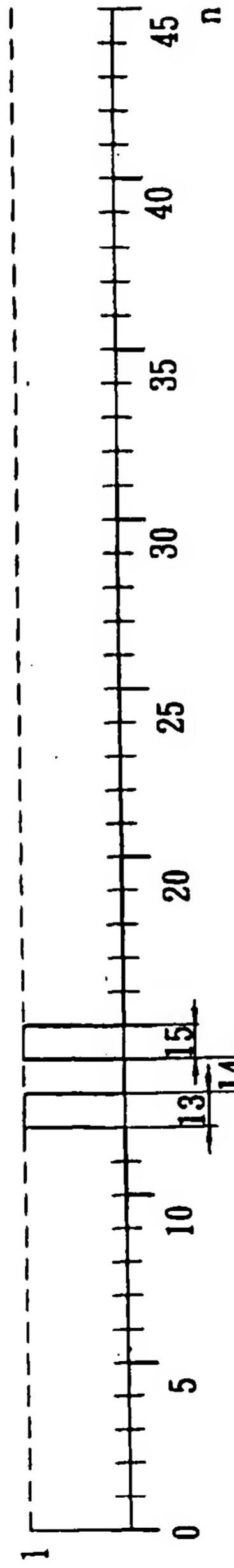


FIG. 3

EP 1 074 949 A1

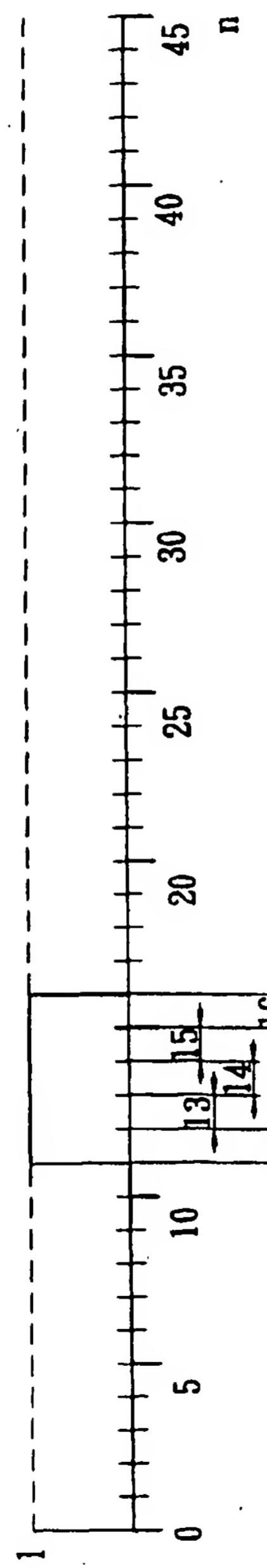


FIG. 4

EP 1 074 949 A1

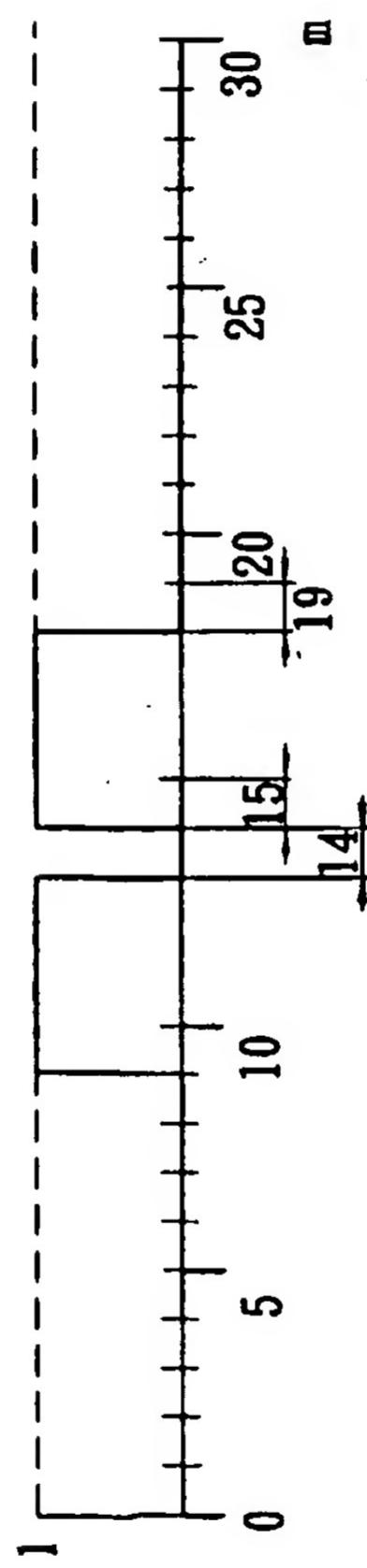


FIG.5



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Application Number
EP 99 30 6179

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THE HAGUE	11 January 2000	Meyl, D	
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